

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Robert H. Dennard, et al.

Examiner: Unassigned

Serial No.: To be assigned

Art Unit: Unassigned

Filed: Herewith

Docket: YOR920020157US2 (15667A)

For: POLYSILICON BACK-GATED
SOI MOSFET FOR DYNAMIC THRESHOLD
VOLTAGE CONTROL

Dated: September 11, 2003

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, applicants submit the following references which applicants believe may be material to the above-identified patent application. A copy of the references which applicants wish to make of record in this case is enclosed herein for the Examiner's convenience along with a listing on Form PTO-1449 attached.

1. U.S. Patent No. 6,074,920, issued June 2000 to Houston;

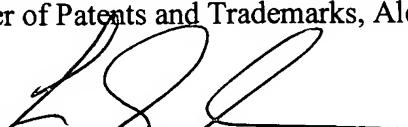
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I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450.

Dated: September 11, 2003


Leslie S. Szivos, Ph.D.

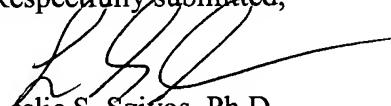
2. U.S. Patent No. 6,391,695, issued May 2002 to Yu; ;
3. U.S. Patent No. 6,441,436, issued August 2002 to Wu et al.; ;
4. U.S. Patent No. 6,383,904, issued May 2002 to Yu; ;
5. U.S. Patent No. 6,392,277, issued May 2002 to Mitani et al.; ;
6. U.S. Patent No. 6,359,312, issued March 2002 to Komatsu.; ;
7. U.S. Patent No. 6,423,599, issued July 2002 to Yu; ;
8. U.S. Patent No. 6,423,754, issued August 2002 to Assaderaghi et al.; and ;
9. U.S. Patent No. 6,275,094, issued August 2001 to Cranford et al.. ;

Pursuant to 37 C.F.R. §1.98 (d), copies of the references listed on the enclosed Form PTO-1449 are not provided since they were previously made record of in the parent application Serial No. 10/235,169 filed on September 5, 2002. References 1-9 were cited by the Patent and Trademark Office in an Office Action dated March 14, 2003

Consideration of this Information Disclosure Statement is respectfully requested, since the information provided herewith may be material to the examination of the present application as defined under 37 C.F.R. § 1.56. This statement is not intended to represent that a search has been performed or that no other art than that identified herein exists.

The instant Information Disclosure Statement is being submitted concurrent with the filing of the present application. Therefore, this filing is made under 37 C.F.R. § 1.97(b)(1). An Information Disclosure Statement filed under 37 C.F.R. § 1.97(b)(1) requires neither certification nor fee.

Respectfully submitted,



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Enclosure PTO Form 1449

<p>LIST OF PRIOR ART CITED BY APPLICANT</p> <p>(Use several sheets if necessary)</p>				Atty. Docket No. YOR920020157US2 (15667A)		Serial No. To be assigned	
				Applicant Robert H. Dennard, et al.			
				Filing Date Herewith		Group Unassigned	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL*		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
	AA	6,074,920	6/2000	Houston			
	AB	6,391,695	5/2002	Yu			
		6,441,436	8/2002	Wu et al.			
		6,383,904	5/2002	Yu			
		6,392,277	5/2002	Mitani et al.			
		6,359,312	3/2002	Komatsu			
		6,423,599	7/2002	Yu			
		6,275,094	8/2001	Cranford et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER				DATE CONSIDERED			
<small>* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							

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